EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2046	280/735.ccls. and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:20
L4	2258	307/10.1.ccls. and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:21
L5	1115	340/436.ccls. and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:21
L6	1492	340/438.ccls. and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:21
L7	4	1 and (control$4 or microcontroller$4 or CPU$2 pr processor$2) and (SPI or (serial with peripheral with interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:22
L8	10	4 and (control$4 or microcontroller$4 or CPU$2 pr processor$2) and (SPI or (serial with peripheral with interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:22
L9	3	5 and (control$4 or microcontroller$4 or CPU$2 pr processor$2) and (SPI or (serial with peripheral with interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:22
L10	14	6 and (control$4 or microcontroller$4 or CPU$2 pr processor$2) and (SPI or (serial with peripheral with interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:22

L11	6	6 and (control$4 or microcontroller$4 or CPU$2 pr processor$2) and (SPI or (serial with peripheral with interface)) and transceiv$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:23
L12	0	5 and (control$4 or microcontroller$4 or CPU$2 pr processor$2) and (SPI or (serial with peripheral with interface)) and transceiv$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:23
L13	2	4 and (control$4 or microcontroller$4 or CPU$2 pr processor$2) and (SPI or (serial with peripheral with interface)) and transceiv$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:23
L14	0	1 and (control$4 or microcontroller$4 or CPU$2 pr processor$2) and (SPI or (serial with peripheral with interface)) and transceiv$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:23
L15	251	(exchang$4 or transfer$4 or interact$4 or interexchang $4) same (control$4 or microcontroller$4 or CPU$2 pr processor$2) same (SPI or (serial with peripheral with interface)) and transceiv$4 and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:41
L16	988	701/36.ccls. and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:41
L17	73	15 and switch$4 and (deploy $4 or redundan$4) and (impact$4 or safe$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:42
L18	11	16 and (control$4 or microcontroller$4 or CPU$2 pr processor$2) and (SPI or (serial with peripheral with interface)) and transceiv$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:42

L19	4	(control$4 or microcontroller$4 or CPU$2 pr processor$2) with (SPI or (serial with peripheral with interface)) with transceiv$4 with (exchang$4 or interchang$4 or transfer$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:55
L20	43	(control$4 or microcontroller$4 or CPU$2 pr processor$2) same (SPI or (serial with peripheral with interface)) same transceiv$4 same (exchang$4 or interchang$4 or transfer$4) and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:58
L21	22	(control$4 or microcontroller$4 or CPU$2 pr processor$2) same (SPI or (serial with peripheral with interface)) same transceiv$4 same (exchang$4 or interchang$4) and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 13:00
S3	53	((wireless or inductive) with transmi$5) and (control$4 or microcontrol$4 or process$4 or microprocess$4) and ((serial with peripheral with interface) or SPI) and (SCON or (safe$4 with (circuit$2 or conductor$2 or control$4))) and (exchang$4 or transfer$4) and switch$4 and (deploy$4 or redundan$4) and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:34
S4	349	((wireless or inductive) with transmi$5) and (control$4 or microcontrol$4 or process$4 or microprocess$4) and (steer$4 with wheel) and (sens$4 or detect$4 or trigger$4) and (exchang$4 or transfer$4) and switch$4 and (deploy$4 or redundan$4) and (impact$4 or safe$4) and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:35

S5	6	((wireless or inductive) with transmi$5) same (control$4 or microcontrol$4 or process $4 or microprocess$4) same (steer$4 with wheel) and (sens$4 or detect$4 or trigger $4) same (exchang$4 or transfer$4) same switch$4 and (deploy$4 or redundan $4) and (impact$4 or safe $4) and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:36
S6	8	S4 and (SPI or (serial with peripheral with interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:40
S7	0	S5 and (SPI or (serial with peripheral with interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:40
S8	53	S3 and (SPI or (serial with peripheral with interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:41
S9	4	S8 and (steer$4 with wheel)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:41
S10	59	kazmierczak-harald.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:45
S11	47	tschentscher-harald.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:46
S12	120	schumacher-hartmut.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:46

S13	251	mattes-bernhard.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:47
S14	1	S10 and ITIC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:47
S15	1	S11 and ITIC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:47
S16	1	S12 and ITIC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:47
S17	1	S13 and ITIC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:47
S18	0	S5 and (SPI or (serial with peripheral with interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 13:04
S19	349	((wireless or inductive) with transmi$5) and (control$4 or microcontrol$4 or process$3 or microprocess$3) and (steer $4 with wheel) and (sens$4 or detect$4 or trigger$4) and (exchang$4 or transfer$4) and switch$4 and (deploy$4 or redundan$4) and (impact $4 or safe$4) and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 13:04

S20	239	((wireless or inductive) with transmi$5) and safe$3 with (control$4 or microcontrol$4 or process$3 or microprocess $3) and (steer$4 with wheel) and (sens$4 or detect$4 or trigger$4) and (exchang$4 or transfer$4) and switch$4 and (deploy$4 or redundan $4) and (impact$4 or safe $4) and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 13:12
S21	239	((wireless or inductive) with transmi$5) and (safe$3 with (control$4 or microcontrol$4 or process$3 or microprocess $3)) and (steer$4 with wheel) and (sens$4 or detect $4 or trigger$4) and (exchang$4 or transfer$4) and switch$4 and (deploy$4 or redundan$4) and (impact $4 or safe$4) and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 13:12
S22	26	((wireless or inductive) with transmi$5) and (safe$3 with (control$4 or microcontrol$4 or process$3 or microprocess $3)) same (steer$4 with wheel) and (sens$4 or detect $4 or trigger$4) and (exchang$4 or transfer$4) and switch$4 and (deploy$4 or redundan$4) and (impact $4 or safe$4) and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 13:13
S23	328	((wireless or inductive) with transmi$5) and (safe$3 with (control$4 or microcontrol$4 or process$3 or microprocess $3)) same (exchang$4 or transfer$4 or switch$4) and (deploy$4 or redundan$4) and (impact$4 or safe$4) and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 13:20

S24	29	((wireless or inductive) with transmi$5) and (safe$3 with (control$4 or microcontrol$4 or process$3 or microprocess $3)) same (exchang$4 or transfer$4 or switch$4) and (SPI or (serial with peripheral with interface)) and (deploy$4 or redundan $4) and (impact$4 or safe $4) and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 13:21
S25	207	((wireless or inductive) with transmi$5) and (control$4 or microcontrol$4 or process$3 or microprocess$3) same (exchang$4 or transfer$4 or switch$4) and (SPI or (serial with peripheral with interface)) and (deploy$4 or redundan$4) and (impact$4 or safe$4) and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 13:22
S26	55	((wireless or inductive) with transmi$5) and (control$4 or microcontrol$4 or process$3 or microprocess$3) same (exchang$4 or transfer$4 or switch$4) same (SPI or (serial with peripheral with interface)) and (deploy$4 or redundan$4) and (impact$4 or safe$4) and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 13:23
S27	3	((wireless or inductive) with transmi$5) and (control$4 or microcontrol$4 or process$3 or microprocess$3) same (exchang$4 or transfer$4 or switch$4) same (SPI or (serial with peripheral with interface)) same (deploy$4 or redundan$4) and (impact $4 or safe$4) and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 13:23

S28	1	(((wireless or inductive) with transmi$5) and (control$4 or microcontrol$4 or process$3 or microprocess$3) and (exchang$4 or transfer$4 or switch$4) and (SPI or (serial with peripheral with interface)) and (deploy$4 or redundan$4)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 13:28
S29	312	(((wireless or inductive) with transmi$5) and (control$4 or microcontrol$4 or process$3 or microprocess$3) and (exchang$4 or transfer$4 or switch$4) and (deploy$4 or redundan$4)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 13:30
S30	12	S29 and (steer$4 with wheel)	US-PGPUB; USPAT	OR	ON	2008/10/16 13:30
S31	1	S10 and redundan$4 and deploy$4 and (control$4 or microprocess$4 or process $4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 14:13
S32	1	S12 and redundan$4 and deploy$4 and (control$4 or microprocess$4 or process $4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 14:13
S33	3	S13 and redundan$4 and deploy$4 and (control$4 or microprocess$4 or process $4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 14:13
S34	1	S11 and redundan$4 and deploy$4 and (control$4 or microprocess$4 or process $4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 14:13
S35	364	(transceiv$4 and (control$4 or microcontrol$4 or process $3 or microprocess$3) and (exchang$4 or transfer$4 or switch$4) and (deploy$4 or redundan$4)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 10:34

| S36 | 10 | (transceiv$4 and (control$4 or microcontrol$4 or process$3 or microprocess$3) and (exchang$4 or transfer$4 or switch$4) and (deploy$4 and redundan$4)).clm. | US-PGPUB; USPAT | OR | ON | 2008/10/17 10:35 |
| S37 | 10 | S35 and (steer$4 with wheel) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/10/17 10:38 |

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